REMARKS

Reconsideration of the application in view of the above amendments and the following remarks is respectfully requested.

The Examiner objects to the abstract as being of improper form. A new abstract is provided herewith on a separate sheet which is in the correct format.

The Examiner objects to the disclosure and specifically recites page 9, line 11 and line 13 which recites a cross sectional line A-A where as in Figure 4A it is labeled as "4B". The Examiner states that on page 10, lines 19 and 22, pad unit should be 130A instead of 132.

We have amended the Detailed Description herewith as requested by the Examiner.

The Examiner rejects Claims 1-3, 5 and 8-9 under 35 U.S.C. 103(a) as being unpatentable over Dingwall in view Mical, et al. The Examiner states that Dingwall teaches the present invention except that he does not teach a switching circuit that sequentially connects the first through n-th input terminals to the first through n-th output terminals respectively when a control signal is at a first logic level and sequentially connects the n-th through first input terminals to first through n-th output terminals respectively when a control signal is at a second logic level. The Examiner states that Mical, et al. teaches a cross-over unit which can place appropriate even or odd numbered pixel signals on respective side buses and concludes that it would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the Mical, et al. approach in the Dingwall apparatus.

We can not agree. Figure 4 of Dingwall corresponds to the prior art Figure 9 of the present application. Furthermore, the cross-over circuit shown in Mical, et al. is a

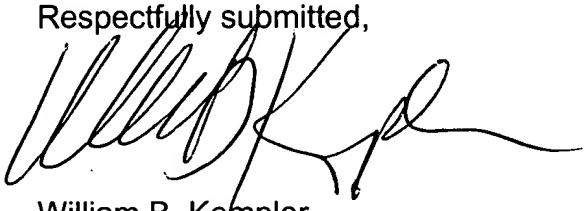
memory circuit which is utilized to improve the resolution of the display, and not to improve the layout for the drivers, in order to save cost. The present invention, the circuits are designed so that they can be placed on the flexible wiring substrate utilizing a single level of wiring by alternating the way in which the input signals are switched to the output signals. This is neither shown or suggested by Dingwall or Mical, et al., either singularly or in combination. Claims 1-10 have been canceled without prejudice and replaced by new Claims 11-26 to clearly recite this feature.

The Examiner rejects Claims 4, 6-7, and 10 under 35 U.S.C. 103(a) as being unpatentable over Dingwall and Mical, et al. as above, and further in view of Voisin, et al.

This rejection is respectfully traversed. Voisin, et al. requires the utilization of a flexible circuit having at least two layers of metalized wiring, see the Abstract, and thus teaches away from the present invention.

Accordingly, Applicants believe that the Application, as amended, is in condition for allowance, and such action is respectfully requested.

Respectfully submitted,



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